

Scholar Details:

S. No	Scholar Name, Designation and Institution	Supervisor Name, Designation and Institution	Year of Reg. & Reg. No.	Title of Research work	Status of PhD (Coursework completed/Synopsis submitted/Thesis submitted/Vivo voce completed)
1.	Mr.J.Dhanansekar AP/ECE Sri Eshwar College of Engineering, Coimbatore.	Dr.Sudha V K Professor, Electronics and Communication Engineering	July 2016 & 16144697235	ASIC Implementation of RRAM by adiabatic Logic for low power applications.	Coursework completed (confirmation received)
2.	Mr.D.Kumar AP/ECE PA College of Engineering and Technology, Pollachi	Dr.Sudha V K Professor, Electronics and Communication Engineering	July 2015 & 1514469853	Image Encryption with lossless / Lossy Compression	Coursework completed (confirmation received)